

2017 IEEE INTERNATIONAL SYMPOSIUM ON ELECTROMAGNETIC COMPATIBILITY, SIGNAL AND POWER INTEGRITY

2017 IEEE INTERNATIONAL SYMPOSIUM ON



Electronics Design of Avionics Hardware using Worst Case Analysis Techniques: An EMC Application and Example

Reinaldo J Perez
Electronic Products Reliability Group
Jet Propulsion Laboratory
California Institute of Technology
August 11, 2017
© 2017 California Institute of Technology.
Government sponsorship acknowledged



Avionics Hardware for Space and The Space Environments

Avionics designers must contend with the space environment in the hardware design

Electronics designs and the resulting hardware are susceptible to the harsh space environment

Since EMC compliance in avionics hardware depends on good electronic design processes to control EMI, it is then obvious that the space environment can also play a role in EMC.

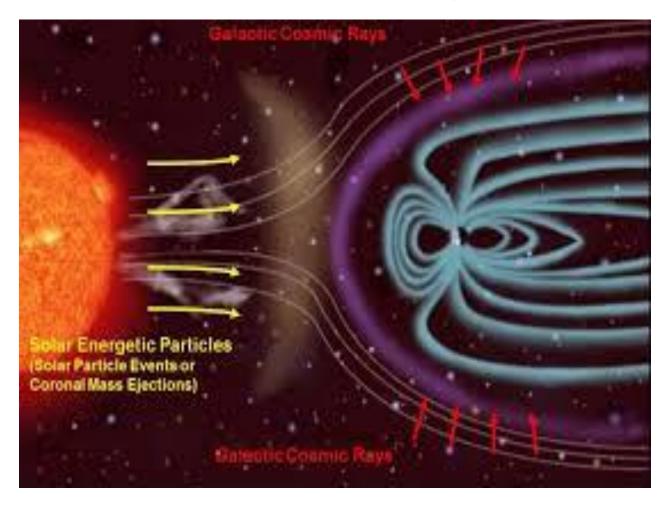
The three most important environmental factors that affect electronics are:

Temperature: -50°C to 150°C swings

Space Radiation: Highly energetic charged particles (ions, electrons,...etc) which can affects electronic components performance.

Aging of Electronic Components: Space missions are long term with no possibilities of repairs or maintenance

Earth and the Space Charged Environment



Energetic charged particles from the sun and cosmos arrive to earth and are influenced by the earth magnetic field

The Space Environment and Electrical Components Parameters Variations

The space environments affect the performance of electrical components by <u>changing components'</u> <u>parameter values</u>. How component parameter values are affected depends on the type of electronic component. The table below shows electronic component type vs. principal source of parameter variation

Environmental Factor	Resistors	Capacitors	Diodes	BJTs	MOSFET	JFET	Dig. IC	Linear ICs	GaAs ICs
Temperature	Х	X	X	X	X	X	Х	X	X
Aging							X	X	
Radiation in (Rads)	>1Meg	>500K	>100K	>5K	>1K	>500K	>1K	>1K	>1Meg
Humidity	Х	X							
Elec. Stress	Х	X	X	X	X	Х			
Mechanical	Х	Х							

The Space Environment and Electrical Components Parameters Variations (Cont.'s)

Each electrical component in avionic hardware has its own distinctive set of parameters that can be affected by the space environment and the table below shows a list of such parameters for each of the mostly used type of components.

Electrical Parameters	Resistors	Capacitors	Diodes	Inductor	BJTs	MOSFET	JFET	Dig. IC	Linear ICs	GaAs ICs
Resistance	Х									
Capacitance		Χ								
Inductance				X						
Supply voltage								X	Х	Х
Input/Intrinsic Voltage(s)					Х	X	X	X	Х	X
Input/Intrinsic Current(s)			х						Х	
Gain					Х	X	Х		Х	
Output Voltage(s)								X	Х	X
Output Current(s)			X							
Propagation Delay(s)								Х	Х	X
Timing Parameters								X	X	X

The Space Environment and Electrical Components Parameters Variations (Cont.'s)

Choosing the Electrical Components: One of the primary tasks for avionics design engineers is to develop an APPROVED components list for the design. The term "approved" means that the chosen components for the design must be first reviewed and then approved (or reject otherwise) after gathering empirical data, vendor data, and test data, and then make an assessment about the components' reliability and tolerance fluctuations under worst case space environmental conditions.

An example addressing temperature tolerances:

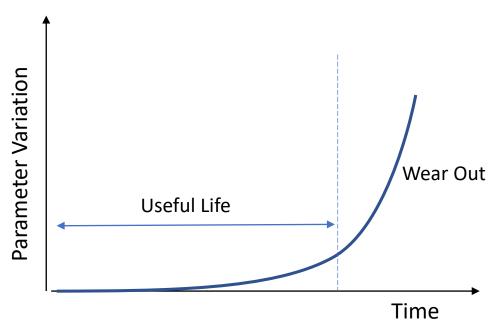
An EMI filter for a data interface contains a capacitor that is 20uF. The part has a tolerance of 10% at 25C. What would be the tolerance of that part, for a temperature fluctuation of -50C to 150C?

The answer is (just for temperature alone!) 11.25% and -10.75%: Therefore, minimum capacitance = 17.85uF and maximum capacitance = 22.2 uF.

The "worst case" changes in capacitance means that you not longer have a 20uF in that EMI filter, hence the filter's performance characteristics will change! You must also address, in the same way, all the other components of the filter.

The Space Environment and Electrical Components Parameters Variations (Cont.'s)

<u>Addressing Aging Effects</u>: Electrical components which are powered are aging continuously. The aging is the result of chemical changes related to the relative energy levels of the reactants that make up the electrical components. These chemical changes causes parameters to drift.

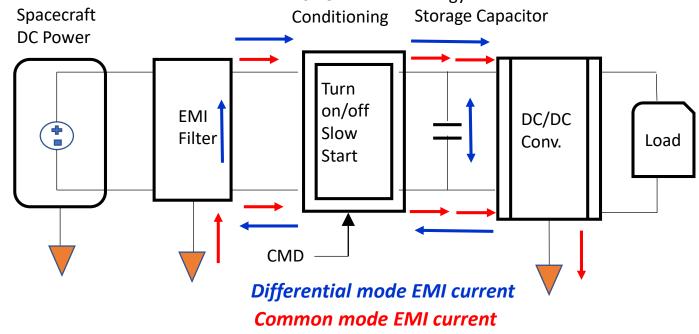


Let's consider the previous capacitor. The capacitor was tested for 200 hrs at 150C and this resulted in a change in capacitance of +/-15%. If this capacitor is used for 10 years at 85C, by how much the capacitance will change. The answer is +/- 3.75%. The overall change in tolerance for temperature and aging combined is: 15% worst case maximum and -14.5% worst case minimum.

Effects of Components Parameters Variations in EMC

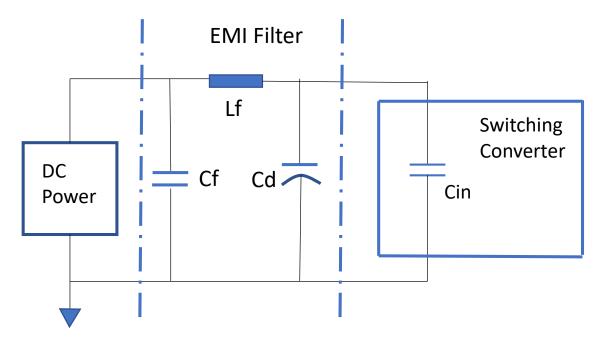
Before EMC principles are implemented in avionics hardware design, the same hardware must first be designed to survive the worst case environmental conditions, otherwise the EMC solutions will fall short.

Consider the EMI filter in the figure below. These filters are designed to attenuate differential and common mode noise produced by their respective currents (shown in figure) and are designed to attenuate such noise by as much as 80 dB/decade over a frequency range beyond cut-off frequency. In order to accomplish this the EMI filter must be designed such that it will perform such functions under worst case space environmental conditions Power Energy



Designing an EMI Filter for a Switching Converter

Let's consider the design of a LP EMI filter for a switching converter. The filter must attenuate EMI noise to at least 65 dbuV. The input capacitance of the switching converter is Cin=20uF. The switching frequency of the converter Fs=800 kHz. The input voltage from the power supply Vin=30V and the output voltage from the switching converter is Vout=3.3V. The maximum output current of the converter is lout=3A. The estimated noise level (measured data) of the switching converter is 120dBuV (ripple voltage). The filter is shown below (in its simplest form, also known as *parallel damping LC*). The required attenuation (Att) in dB is 120-65= 55 dB



Designing an EMI Filter for a Switching Converter

The inductor Lf defines the resonant frequency. For low current applications (3A is a low current application) Lf should be between 1uH to 10uH (usually). Let's choose 1uH. We now calculate Cf. The Cf capacitance is constrained by two capacitance Cfa and Cfb, as shown below:

$$C_{fa} = \frac{C_{in}}{\left(C_{in} * L_f * \left(\frac{2\pi F_s}{10}\right)^2 - 1\right)}$$
(1)

$$C_{fb} = \frac{1}{L_f} * \left(\frac{10^{\frac{Att(dB)}{40}}}{2\pi F_S}\right)^2 \tag{2}$$

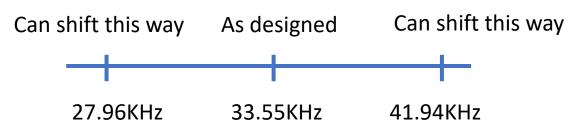
The first formula for Cfa ensures that the resonance frequency of the EMI input filter is at least one decade below the switching frequency Fs. The second formula for Cfb is derived from an approximation that ensures proper attenuation of the EMI filter (in this case we want 40 dB attenuation per decade per equation 2). Select the higher value of Cfa and Cfb because both conditions must be met. In the above formulas, Fs is in kHz, Cfa & Cfb are in uF and Lf is in uH. The results are Cfa=2.96uF, Cfb=22.5uF. Therefore, Cfb=Cf=22.5uF.

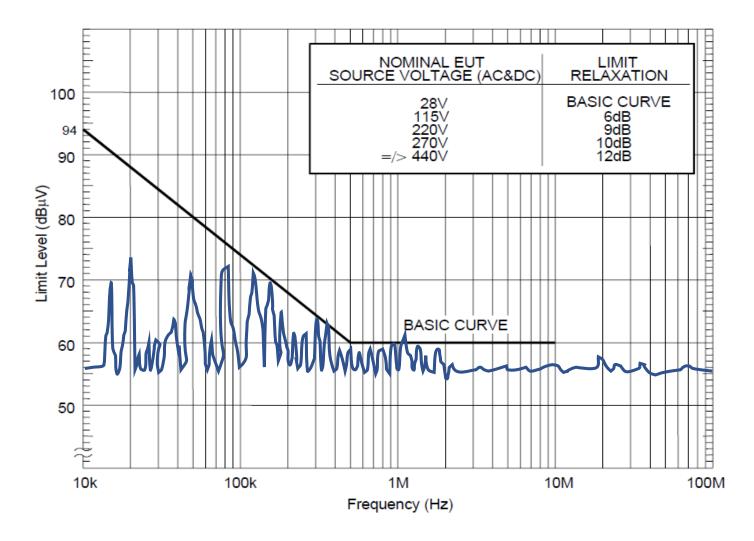
Designing an EMI Filter for a Switching Converter

The cut off frequency Fc of the low pass filer (LPF) is $1/2\pi SQRT$ (Lf*Cf) = 33.55 kHz. The capacitor Cd is a tantalum capacitor with ESR. Cd is a damping capacitor and the damping is needed because the EMI output impedance can be very high at the resonance frequency (i.e. the Q of the LPF formed by Cin and Lf can be very high). The value of Cd should be grater then 4*Cin. So in our case it should at least be 4*20uF=80uF. The ESR of the chosen capacitor can be around from SQRT (Lf/Cin)=112 milli-ohms. The purpose of the ESR is to reduce the peak output impedance at Fc of the LPF. The Cd blocks the dc component of the input voltage and avoids excessive power dissipation on ESR.

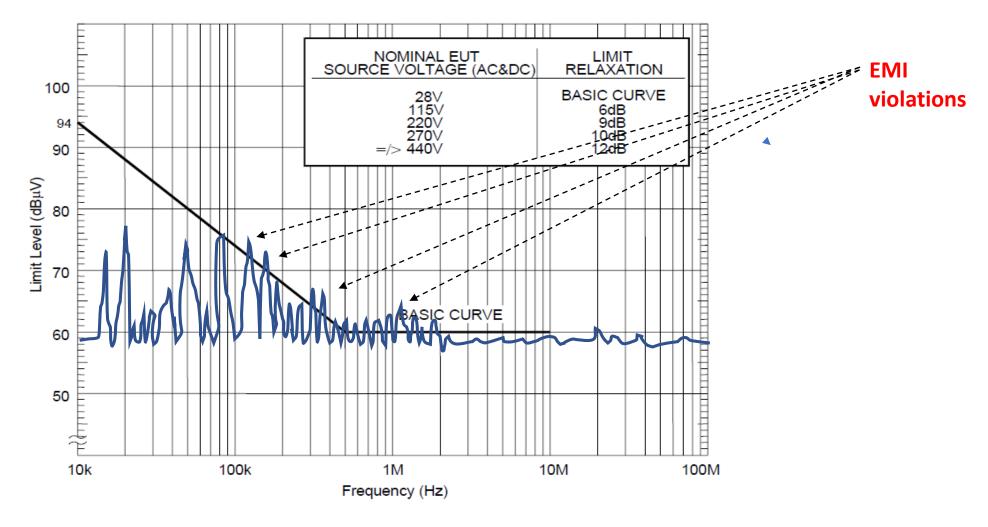
Let's assume now that Cf and Lf change by 20% due to space environmental conditions. This means that the cuff off frequency of 33.55 kHz can fluctuate between Fc_min = 27.96 kHz and Fc_max = 41.94 kHz.

What does this means from an EMI point of view? It means that the "damping" capability of the EMI filter to diminish ripple noise will be <u>shifted</u> as time goes by and under worst case environmental conditions.





At the beginning of life (BOL) of its application, the EMI Filter, as originally designed, performs well in controlling EMI Emissions.

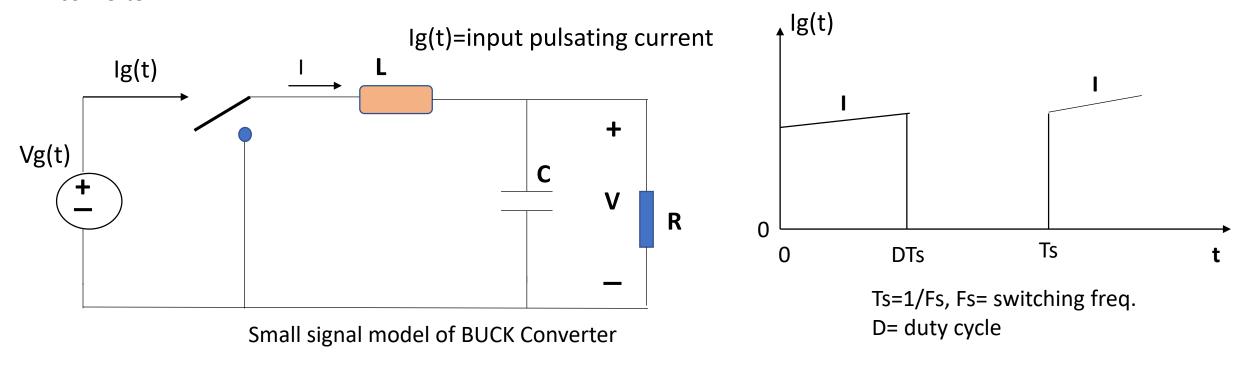


At the end of life (EOL) of its application, the EMI Filter, as originally designed, NO LONGER performs well in controlling EMI Emissions.

FIGURE CE102-1. CE102 limit (EUT power leads, AC and DC) for all applications.

Though EMI violations may not be a big issue, we need to go beyond than just looking at EMI noise and explore the overall effect of parameters variations of EMI filter design. Therefore, let's consider the ensemble of the EMI filter couple with a switching power converter.

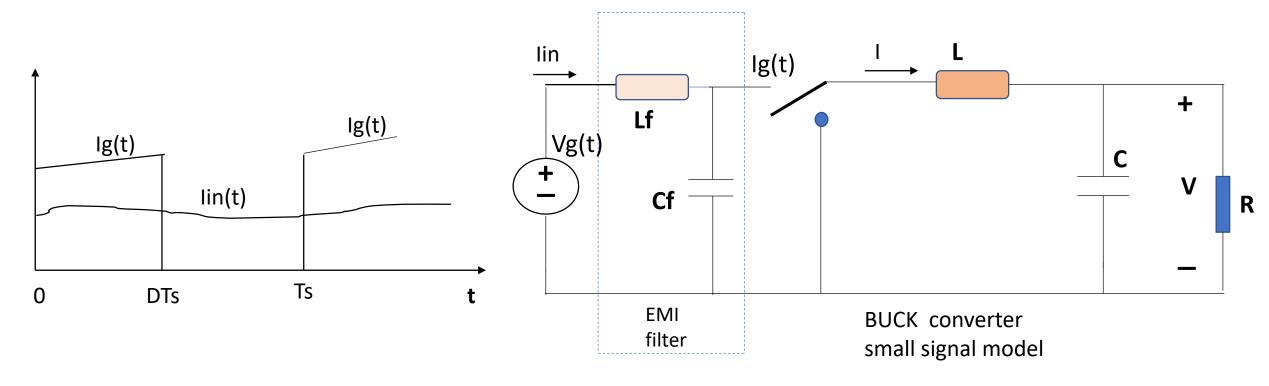
Let's consider an actual application. We start with some simplified theory on converter design. We choose the Buck converter:



The Fourier series of Ig(t) is: $Ig(t) = DI + \sum_{k=1}^{\infty} \frac{2I}{k\pi} Sin(k\pi D) Cos(k\omega t)$. High frequency current harmonics of large amplitude are injected back into Vg(t) source

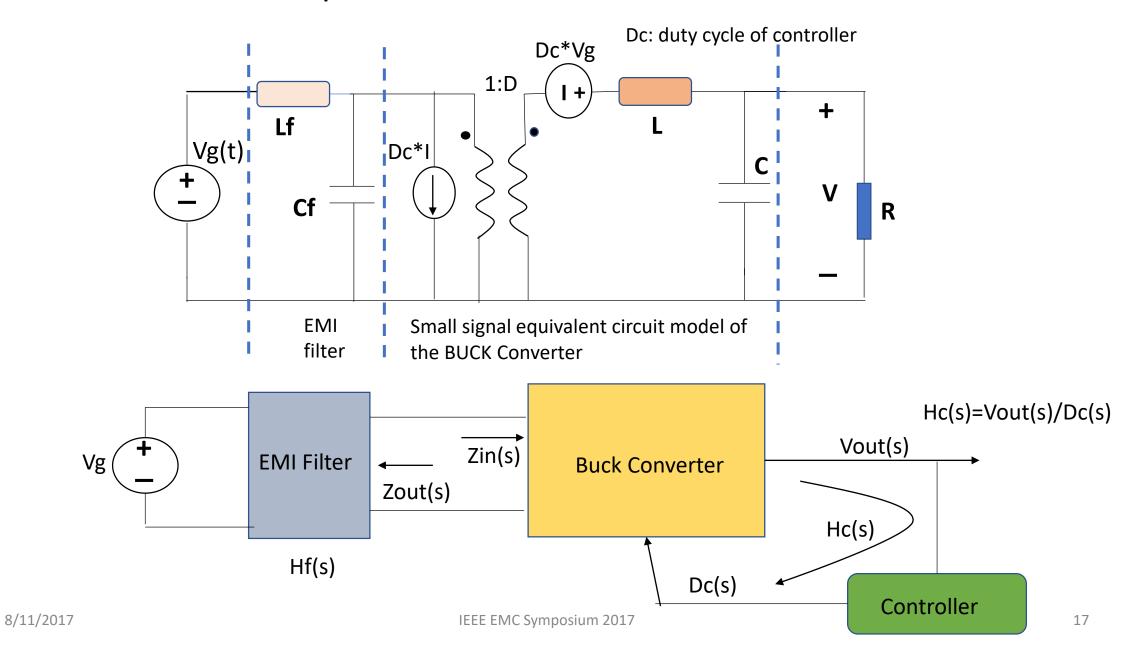
8/11/2017 IEEE EMC Symposium 2017 15

Adding a low Pass EMI to the Converter Filter



Magnitude and phases of the input current harmonics are modified by input filter transfer function H(s):

$$\lim(\mathsf{t}) = H(0)DI + \sum_{k=1}^{\infty} \|H(kj\omega)\| \frac{2I}{k\pi} Sin(k\pi D) Cos(k\omega t + \angle H(kj\omega))$$



We now can state the overall transfer function for the output of the Buck converter. Using the previous diagram we get.

$$Hcs(s) = Hc(s)_{original} * \frac{\left(1 + \frac{Z_{out}(s)}{Z_{in}(s)atD_C(s) = 0}\right)}{\left(1 + \frac{Z_{out}(s)}{Z_{in}(s)atV_{out}(s) = 0}\right)}$$

where:

Hc(s)_{original} is the transfer function of the buck converter before adding the input filter

Z_{out}(s) is the output impedance of the EMI filter

 $Z_{in}(s)$ at Dc(s)=0 is the converter input impedance when we set the input of the controller to zero

Z_{in}(s) at Vout(s)=0 is the converter input impedance when was short out the output

Therefore, if we want the converter output impedance NOT to be substantially affected by the EMI input filter, the following conditions must be satisfied:

$$||Z_{out}(s)|| \ll ||Z_{in}(s)at Dc(s)| = 0||$$

$$||Z_{out}(s)|| \ll ||Z_{in}(s)at\ Vout(s) = 0||$$

The above expression now becomes a design criteria.

In the same manner if:

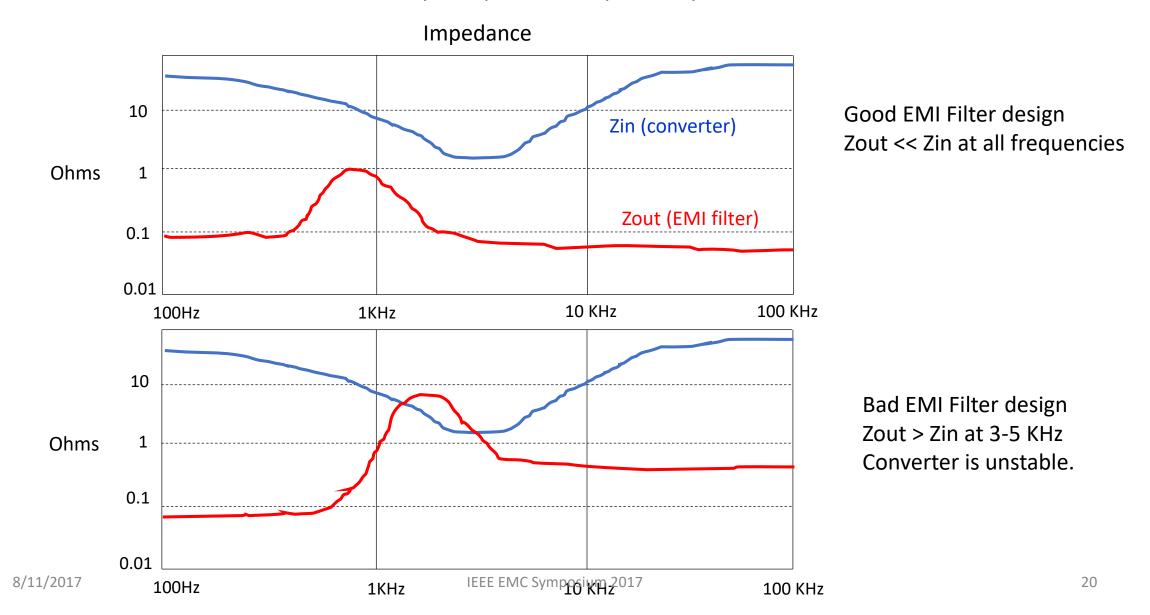
$$||Z_{out}(s)|| > ||Z_{in}(s)at Dc(s)| = 0||$$

$$||Z_{out}(s)|| > ||Z_{in}(s)at\ Vout(s) = 0||$$

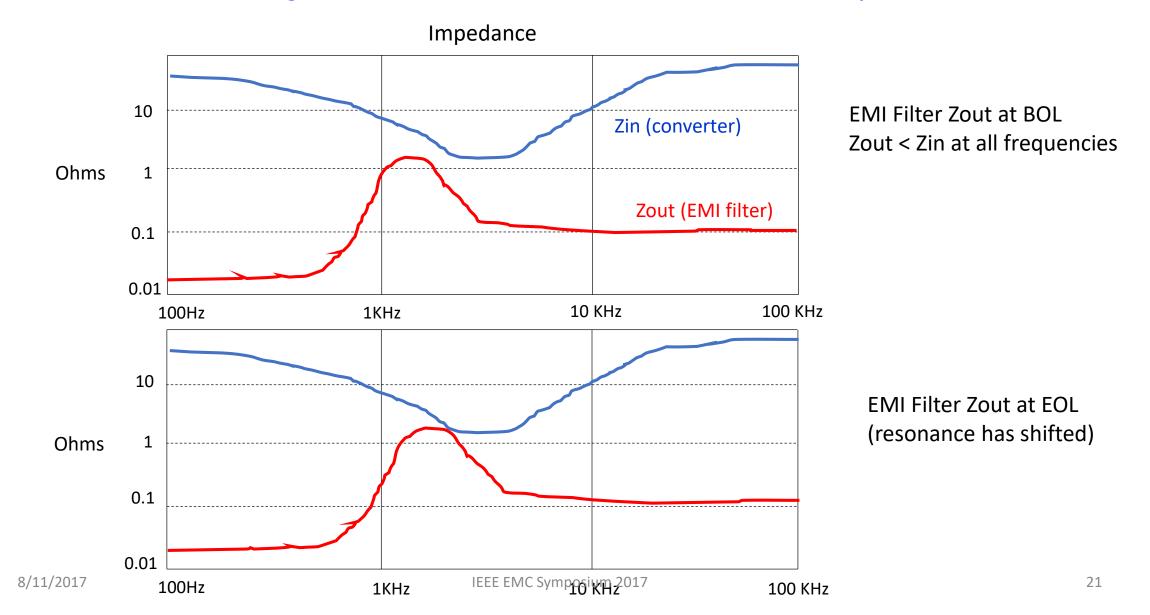
The design of the converter becomes unstable.

Therefore the design of the EMI filter has a direct impact in the stability of the converter!

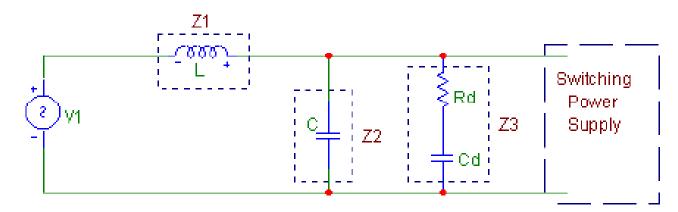
So what does all that means? The best way is to present this pictorially



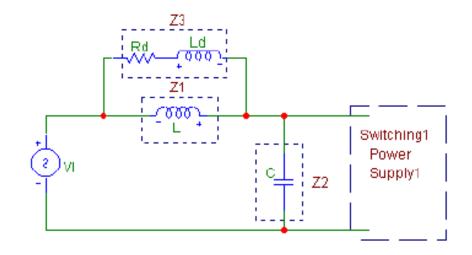
An EMI Filter Zout will change over time due to environmental effects, and can cause problems at EOL

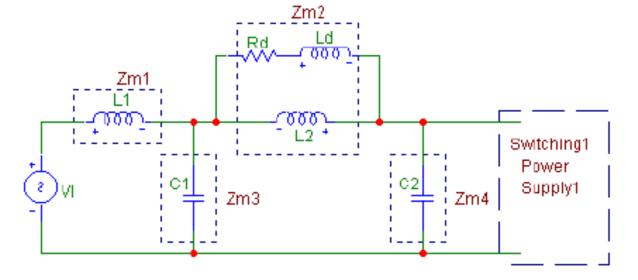


Types of Typical EMI Filters



Parallel Damped Filter

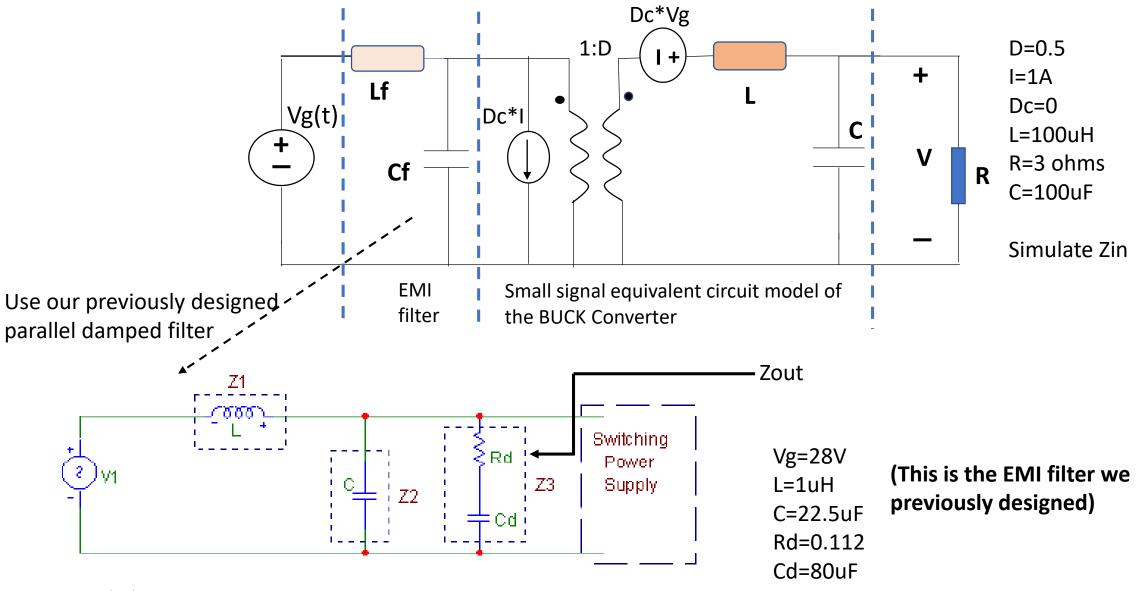




Series Damped Filter (good at LF not so good at HF attenuation)

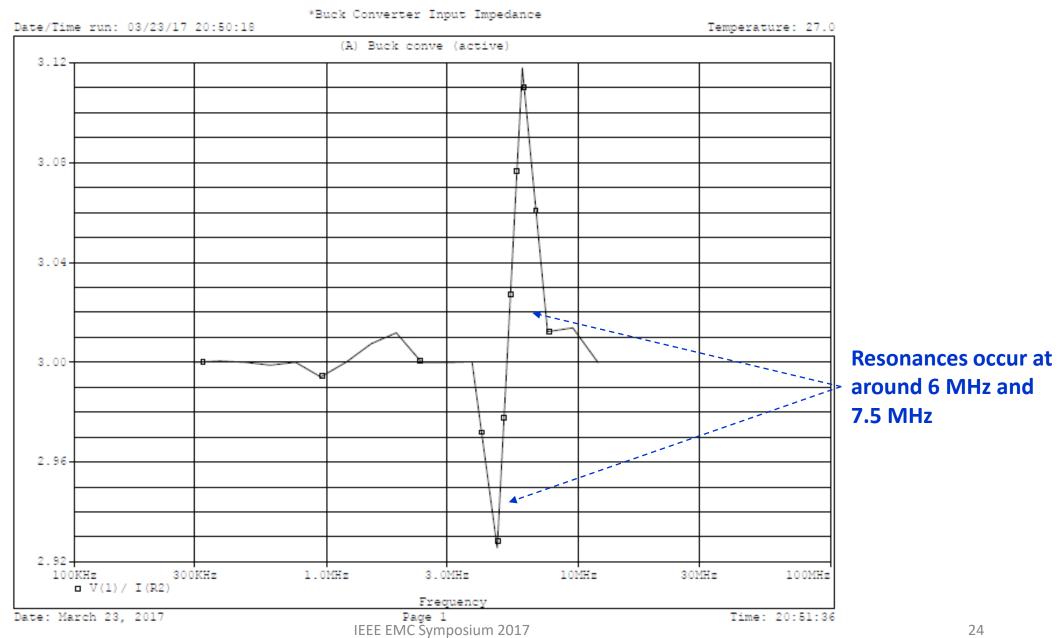
Two-Section Input Filter (allows higher attenuation at HF and uses smaller values of components. Reduce cost)

Analysis Example



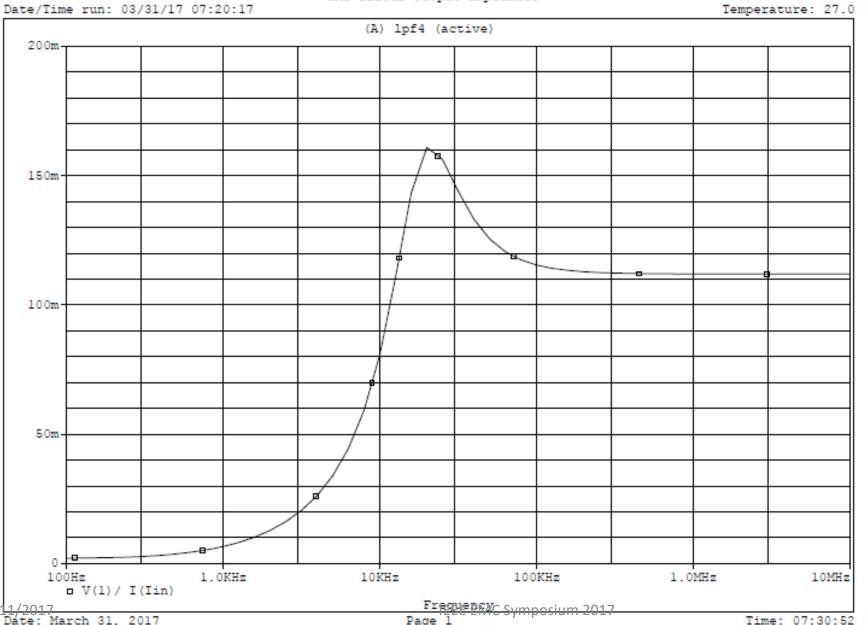
8/11/2017

Zin: Input Impedance of Converter



Zout: Output Impedance of EMI Filter

*EMI filter output impedance



Resonance occurs at within 20-35 kHz.
Well below the resonance frequencies for the converter . Also Zout < Zin at all frequencies.

Conclusion: Very good design of EMI filter. Even if there are large worst case variations in components parameters for the filter. The EMI filter will perform well till EOL.

Conclusions

In aerospace systems electronics are exposed to extreme environmental effects. It is important to assess how such environmental effects can affect the design and the performance of aerospace electronics, including aerospace electronics hardware that has been designed to control EMI. In general EMC assurance can be compromised during long missions if efforts are not spent first in addressing extreme environmental effects during the design phase of electronics hardware.

An example of these environmental effects was addressed in the design and use of an EMI filter and two crucial aspects were discussed: the capability of the EMI filter to suppress noise, and the role that the EMI filter plays in the stability of the power converters.